PATENT NUMBER

J1031 U.S. PTO
09/773874
01/31/01

Class	Subclass
ISSUE CLASSIFICATION	

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
TR. SCANNED NSO Q.A. AM	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/773874		700 710	240	2121 2187	Mason

APPLICANTS

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TITLE

System on a chip having system bus, external bus, and bus arbiter with programmable priorities for both buses, software, and method for assigning programmable priorities

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION									
								Continued on Issue Slip Inside File Jacket	

☐ **TERMINAL DISCLAIMER**	**DRAWINGS**			**CLAIMS ALLOWED**	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) (Date)			**NOTICE OF ALLOWANCE MAILED**	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____					
				ISSUE FEE	
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